

FORM HDP-1449 (Based on Form PTO-1449)

PATENT AND TRADEMARK OFFICE
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Sheet 1 of 1



ATTORNEY DOCKET NO.	SERIAL NO.
8947-000063/US/CPA	10/830,041
APPLICANT	
Joong-Chul YOON et al.	
FILING DATE	GROUP
April 23, 2004	2193

U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	/C.N./	2004-0252829 A1	December 16, 2004	SON		December 17, 2003

FOREIGN PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	Translation No

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	Document Description
2.	/C.N./	European Examination Report dated February 28, 2007, for European Application No. 04252390.2-1243.
3.		Wang, P. A. et al: "New VLSI Architectures Of RSA Public-Key Cryptosystem" June 9, 1997 Vol. 3, pages 2040-2043 (duplicated)
4.		Behrooz, Parhami: "High-Radix Multipliers" Computer Arithmetic: Algorithms And Hardware Design, 2000, page 159, paragraph 10.2-161, figure 10.6
5.		Tenca, A. F. et al: "High-radix Design of A Scalable Modular Multiplier" cryptographic Hardware and Embedded Systems , 3 rd International Workshop, May 14, 2001 Vol. 2162, pages 185-201

JAC/LFG/cm

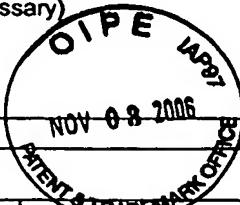
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April 23, 2004	2124

U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
/C.N./		US 2002/0172355 A1	November 21, 2002	Lu et al.		April 4, 2001

FOREIGN PATENT DOCUMENTS

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/C.N./		Behrooz, Parhami: "High-Radix Multipliers" Computer Arithmetic: Algorithms And Hardware Design, 2000, page 159, paragraph 10.2-161, figure 10.6
/C.N./		Tenca, A. F. et al: "High-radix Design of A Scalable Modular Multiplier" cryptographic Hardware and Embedded Systems, 3 rd International Workshop, May 14, 2001 Vol. 2162, pages 185-201
/C.N./		Post, Katharina: European Search Report dated September 15, 2006

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